

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jenny M. Pelner

Title: METHODS AND APPARATUS TO TEST COMPUTER SYSTEM COMPONENTS (AS

AMENDED)

Docket No.: 884.616US1

Filed: February 11, 2002

Examiner: Marc M. Duncan

Customer No.: 21186

Due Date: December 19, 2005

Group Art Unit: 2113

Serial No.: 10/073,546

Confirmation No.: 5100

Commissioner for Patents

Attn: MAIL STOP ISSUE FEE

P.O. Box 1450

Alexandria, VA 22313-1450

Notice of Allowance Date:

September 19, 2005

We are transmitting herewith the attached:

X A check in the amount of \$1400.00 to cover the Large Entity Issue Fee Payment.

X A check in the amount of \$3.00 to cover the Extra Patent Copies Fee (1 copy).

X Issue Fee Transmittal (Form PTOL-85).

X A check in the amount of \$300.00 to cover the Publication Fee Payment.

X A return postcard.

X Other: Communication under 37 C.F.R § 1.312(a) (2 pgs.).

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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number: 21186

Ann M. McCrackin

Reg. No. 42,858

AMM1:CMG:cfh

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Attn - MAIL STOP ISSUE FEE, P.O. Box 1450, Alexandria, VA 22313-1450, on this 14 day of December, 2005.

PATENT

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Title:

METHODS AND APPARATUS TO TEST COMPUTER SYSTEM COMPONENTS

(AS AMENDED)

Assignee:

**Intel Corporation** 

Customer Number: 21186

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## COMMUNICATION UNDER 37 C.F.R. § 1.312(a)

In response to the Notice of Allowance and Notice of Allowability mailed September 19, 2005, Applicants note the following error:

The title of the application as amended in the Response to Restriction Requirement and Supplemental Preliminary Amendment filed on August 31, 2004, is not correctly listed on the Notice of Allowance. The title of the application, as amended, should be listed as "METHODS TO COUPLE INTEGRATED CIRCUIT PACKAGES TO BONDING PADS HAVING VIAS" rather than "METHOD AND APPARATUS FOR COUPLING INTEGRATED CIRCUIT PACKAGES TO BONDING PADS HAVING VIAS."

Applicants respectfully request that the Examiner issue a supplemental notice of allowability that correctly identifies the amended application title as "METHODS TO COUPLE INTEGRATED CIRCUIT PACKAGES TO BONDING PADS HAVING VIAS."

It is believed that no fee is necessary for the present submission. However, if any fees are deemed necessary, please charge any additional fees or credit any overpayment to Account No. 19-0743.

AMENDMENT UNDER 37 C.F.R. § 1.312(a)

Serial Number: 10/073,546 Filing Date: February 11, 2002

Title: METHODS AND APPARATUS TO TEST COMPUTER SYSTEM COMPONENTS (AS AMENDED)

Assignee: Intel Corporation

Please contact Applicants' representative, Walter W. Nielsen (based in Phoenix, Arizona) at (602) 298-8920, or Applicants' below-named representative (based in Minneapolis, Minnesota) at (612) 349-9592 if any assistance is required in this matter.

Respectfully submitted,

JENNY M. PELNER

By her Representatives, SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. Attorneys for Intel Corporation P.O. Box 2938 Minneapolis, Minnesota 55402 (612) 349-9592

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Date Dec. 14, 2005

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Ann M. McCrackin Reg. No. 42,858

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